

Amendments to the Claims:

1 -- 26. (cancelled)

27. (new) A light emitting device calibration system for calibrating a light emitting
5 device in an optical disc drive, the light emitting device calibration system
comprising:

a laser diode installed within the optical disc drive being the light emitting
device to be calibrated;

10 a microprocessor electrically coupled to the light emitting device for controlling
power of the light emitting device by changing values of a drive signal,
receiving a power indication signal corresponding to light emitted by the light
emitting device, and determining a power relationship relating values of the
15 drive signal to powers of the light emitting device according to the power
indication signal for each of the values of the drive signal during a calibration
mode;

20 a light detector for detecting the light emitted by the light emitting device to
generate an analog signal being directly proportional to the light emitted by the
light emitting device;

25 a signal calibration circuit having a predetermined reference voltage being
coupled between the light detector and the microprocessor for generating the
power indication signal having an inverse relationship with the analog signal
such that when the analog signal is at the state of no light was emitted by the
light emitting device, the power indication signal reaches at a predetermined
maximum value, which is a function of the predetermined reference voltage; and

30 a non-volatile memory for storing the power relationship determined by the
microprocessor during the calibration mode, wherein the microprocessor uses
said power relationship to control values of the drive signal according to desired

powers of the light emitting device during a normal operation.

28. (new) The light emitting device calibration system of claim 27, wherein during the calibration mode, the microprocessor adjusts a value of the drive signal so that
5 the light emitting device does not emit any light, calculates a gain of the light emitting device calibration system by measuring a sampled maximum value of the power indication signal as detected by the microprocessor corresponding to the predetermined maximum value of the power indication signal, and correct
10 the power indication signals as measured by the microprocessor for each of the plurality of values of the drive signal according to the obtained gain.

29. (new) The light emitting device calibration system of claim 28, wherein during the calibration mode, when the analog signal is at the state of no light was emitted by the light emitting device, the power indication signal reaches at a
15 predetermined maximum value, which is substantially equal to two times of the predetermined reference voltage.

30. (new) The light emitting device calibration system of claim 29, wherein the signal calibration circuit comprises:

20 an operational amplifier having an inverting terminal, a non-inverting terminal, and an output terminal, wherein the output terminal is for outputting the power indication signal;

25 a voltage reference source being the predetermined voltage value coupled to the non-inverting terminal;

a first resistor having a first end coupled to the analog signal outputted by the power meter, and a second end coupled to the inverting terminal; and

30 a second resistor having a first end coupled to the inverting terminal, and a second end coupled to the output terminal.

31. (new) The light emitting device calibration system of claim 29, wherein during the calibration mode, the gain is calculated by the microprocessor as substantially equal to two times of the predetermined voltage value divided by the sampled maximum value of the power indication signal, which is detected by the microprocessor.
32. (new) The light emitting device calibration system of claim 29, wherein during the calibration mode, the microprocessor is further to correct sampled power indication signal values, which are measured by the microprocessor, and a corrected power indication value is substantially equal to that two times of the predetermined voltage value subtract of the value, which is obtained from the gain multiplying with a sampled power indication signal value.
33. (new) The light emitting device calibration system of claim 28, wherein power indication signal is a digital signal.
34. (new) The light emitting device calibration system of claim 33, wherein the power indication signal complies with a transmission standard, and the microprocessor includes a digital interface complying with the transmission standard.
35. (new) The light emitting device calibration system of claim 34, wherein the transmission standard is RS-232 or USB.
36. (new) The light emitting device calibration system of claim 28, wherein the non-volatile memory is a EEPROM or a FLASH.
37. (new) The light emitting device calibration system of claim 28, wherein the light detector is a power meter having a photo sensor for receiving the light emitted by the light emitting device, and the power meter outputs the analog signal corresponding to an intensity of the light received at the photo sensor.

38. (new) A method of calibrating a light emitting device in an optical disc drive, the method comprising:

providing a laser diode installed within the optical disc drive being the light emitting device to be calibrated;

5 controlling power of the light emitting device by changing values of a drive signal to the light emitting device during a calibration mode;

receiving a power indication signal corresponding to light emitted by the light emitting device;

10 determining a power relationship relating values of the drive signal to powers of the light emitting device according to the power indication signal for each of the values of the drive signal;

detecting the light emitted by the light emitting device to generate an analog signal being directly proportional to the light emitted by the light emitting device;

15 providing a predetermined reference voltage;

generating the power indication signal having an inverse relationship with the analog signal such that when the analog signal is at the state of no light was emitted by the light emitting device, the power indication signal reaches at a predetermined maximum value, which is a function of the predetermined reference voltage; and

20 storing the power relationship determined during the calibration mode for controlling values of the drive signal according to desired powers of the light emitting device in a normal operation mode.

25 39. (new) The method of claim 38, further comprising:

adjusting a value of the drive signal till the light emitting device does not emit any light;

30 calculating a gain of the light emitting device calibration system by measuring a sampled maximum value of the power indication signal as detected corresponding to the predetermined maximum value of the power indication signal; and

utilizing the gain to correct the power indication signals for each of the

values of the drive signal.

40. (new) The method of claim 39, wherein when the analog signal is at the state of no light was emitted by the light emitting device, the power indication signal reaches a predetermined maximum value, which is substantially equal to two times of the predetermined reference voltage.
41. (new) The method of claim 40, further comprising providing a signal calibration circuit for generating the power indication signal, wherein the signal calibration circuit comprises an operational amplifier having an inverting terminal, a non-inverting terminal, and an output terminal; a voltage reference source being the predetermined voltage value coupled to the non-inverting terminal; a first resistor having a first end coupled to the analog signal outputted by the power meter, and a second end coupled to the inverting terminal; and a second resistor having a first end coupled to the inverting terminal, and a second end coupled to the output terminal.
42. (new) The method of claim 40, further comprising calculating the gain being substantially equal to two times of the predetermined voltage value divided by the sampled maximum value of the power indication signal.
43. (new) The method of claim 40, further comprising correcting sampled power indication signal values to generate corrected values being substantially equal to that two times of the predetermined voltage value subtracts the gain multiplying with a sampled power indication signal value.
44. (new) The method of claim 39, wherein power indication signal is a digital signal.
45. (new) The method of claim 44, wherein the power indication signal complies with a transmission standard.
46. (new) The method of claim 45, wherein the transmission standard is RS-232 or

USB.

47. (new) The method of claim 39, further comprising storing the power relationship in a EEPROM or a FLASH.

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48. (new) The method of claim 39, providing a power meter having a photo sensor for receiving the light emitted by the light emitting device and outputting the analog signal corresponding to an intensity of the light received at the photo sensor.